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( Not for submission under 37 CFR 1.99)

Application Number	10806866
Filing Date	2004-03-22
First Named Inventor	Dimpsey et al.
Art Unit	2185
Examiner Name	Savla, Arpan P.
Attorney Docket Number	AUS920040065US1

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IAS/	9	6119075		2000-09-12	Dean et al.	
	10	6189141	B1	2001-02-13	Benitez et al.	
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	15	5794028		1998-08-11	Tran	
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	5	FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490.	<input type="checkbox"/>
	6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.	<input type="checkbox"/>
	7	SCHMIDT et al., "Profile-Directed Restructuring of Operating System Code", IBM Systems Journal, 1998, Vol. 37, No. 2, pp. 270-297.	<input type="checkbox"/>
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/AS/	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>
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/AS/	9	6256771	B1	2001-07-03	O'Neil et al.	
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/AS/	1	20030135719	A1	2003-07-17	DeWitt, Jr. et al.	
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